

**AMENDMENTS TO THE CLAIMS**

Claims 1-15 are pending in the instant application. Claim 1 is an independent claim and Claims 2-15 are dependent claims.

Listing of claims:

1.-13. (Cancelled)

14. (Currently Amended) ~~The system according to claim 1, type A~~  
system for reducing noise in a chip, the system comprising:

a substrate layer integrated within the chip;

a transistor layer integrated within the chip, which is shielded from said  
substrate layer by a shielding layer, wherein said shielding layer reduces transfer  
of noise in the chip;

at least one transistor of a first transistor type that couples said transistor  
layer to said shielding layer;

a positive potential of a quiet voltage source that is coupled to said at least  
one transistor of said first transistor type; and

a noisy voltage source coupled to said at least one transistor of a first transistor type.

15. (Previously Presented) The system according to claim 14, wherein said noisy voltage source is coupled to a source of said at least one transistor of a first transistor type.